

Crystal oscillator

SEIKO EPSON CORPORATION

CRYSTAL OSCILLATOR (SPXO) OUTPUT : CMOS



Product Number (please contact us) SG2016CAN: X1G004801xxxx00 SG-210STF: X1G004171xxxx00 SG3225CAN: X1G005961xxxx15 SG5032CAN: X1G004451xxxx00 SG7050CAN: X1G004481xxxx00

SG2016 / 3225 / 5032 / 7050CAN SG-210STF

- Frequency
- Supply voltage
- Function
- : 1.8 V to 3.3 V Typ. Standby(\overline{ST}) 2

20 standard frequencies

• Operating temperature : -40 °C to +105 °C

:

SG2016CAN SG-210STF





(3.2 x 2.5 mm)



(2.5 x 2.0 mm) (2.0 x 1.6 mm)

SG5032CAN (5.0 x 3.2 mm)

(7.0 x 5.0 mm)

Specifications (characteristics)

Item	Symbol	Specifications				Conditions / Remarks				
Output frequency	fo	14.7456 MHz 16 25 MHz 26	MHz10 MHzMHz20 MHzMHz27 MHzMHz48 MHz	12 MHz 24 MHz 32 MHz 50 MHz	z 24.576 MHz z 33.33 MHz					
		1.60 V to 3.63 V				4 MHz \leq fo \leq 50 MHz, T_use = +105 °C Max.				
Supply voltage	Vcc	1.71 V to 3.63 V				fo = 72 MHz, T_use = +85 °C Max.				Refer to Figure 1
		2.25 V to 3.63 V				fo = 72 MHz, T_use = +105 °C Max.			gaio	
Storage temperature	T ata	-55 °C to +125 °C				SG2016CAN				
	T_stg	-40 °C to +125 °C				All others				
Operating temperature	T_use	-20 °C to +70 °C	C, -40 °C to +85 °C	;, -40 °C	to +105 °C	See of fi	gure *1			
Fraguanay talaranga	f tol	±25 × 10 ⁻⁶				-20 °C to +70 °C				
Frequency tolerance	f_tol	±50 × 10 ⁻⁶				-40 °C to +85 °C, -40 °C to +105 °C				
		V _{CC} = 1.8 V ± 10 %	$V_{CC} = 2.5 V \pm 10$	% Vc	c = 3.3 V ± 10 %					
		1.5 mA Max.	1.6 mA Max.		1.8 mA Max.	No load condition, 4 MHz \leq fo \leq 20 MHz				
Current consumption	I _{CC}	1.8 mA Max.	2.0 mA Max.		2.2 mA Max.	No load condition, 20 MHz < fo \leq 40 MHz				
		2.1 mA Max.	2.4 mA Max.		2.6 mA Max.	No load condition, 40 MHz < fo \leq 50 MHz				
		2.4 mA Max.	2.8 mA Max.		3.0 mA Max.	No load condition, fo = 72 MHz				
Stand-by current	I_std	2.1 μA Max. 2.5 μA Max. 2.7 μA Max. ST =GND								
Symmetry	SYM	45 % to 55 %			50 % V _{CC} level, L_CMOS \leq 15 pF					
	Vон	90 % V _{CC} Min.				1.8 V ± 10 % -1.5 mA	2.5 V ± 10 % -3 mA	3.3 V ± 10 -4 mA		
	Vol	10 % V _{cc} Max.			I _{OH}	-1.5 mA	-3 mA 3 mA	-4 mA 4 mA		
Output voltage	V _{OH-2}	V _{CC} - 0.4 V Min.				1.8 V±10 % -3 mA	2.5 V±10 % -4 mA	3.3 V±10		
	V _{OL-2}	0.4 V Max.			I _{OH}	-3 mA	-4 mA 4 mA	-6 mA 6 mA		
Output load condition (CMOS)	L_CMOS	15 pF Max.								
Input voltage	VIH	80 % V _{CC} Min.				- ST terminal				
	VIL	20 % V _{CC} Max.								
Rise time and Fall time	tr / tf	3 ns Max. 3.5 ns Max. (@1.8 V±10 %)				20 % V _{CC} to 80 % V _{CC} level, L_CMOS = 15 pF				
Start-up time	t_str	3 ms Max.				T = 0 at 90 % V _{CC}				
Frequency aging	f_age	±3 × 10 ⁻⁶ / year Max. +25 °C			+25 °C,	First year				

[Model: SG2016/3225/5032/7050CAN]

Product name SG2016 C AN 25.00000MHz T J H A (Standard form) 1 2 3 4567 ①Model ②Output(C: CMOS) ③Frequency ④Supply voltage ⑤Frequency tolerance ⑥Operating temperature range ⑦Internal identification code("A" is default)

Supply voltage *See Figure 1		5Fre	⑤Frequency tolerance / ⑥Operating temperature range			
Т	T 1.8 V to 3.3 V Typ.		±25 × 10 ⁻⁶ / -20 °C to +70 °C			
Κ	K 2.5 V to 3.3 V Typ.		±50 × 10 ⁻⁶ / -40 °C to +85 °C			
		JH	±50 × 10 ⁻⁶ / -40 °C to +105 °C			

3.63 105°C 2.25 85°C 1.71 1.60 f_o[MHz] 50 72

Vcc[V

* Please refer to Product number list on Full Data Sheet for available frequencies

[Model : SG-210STF]

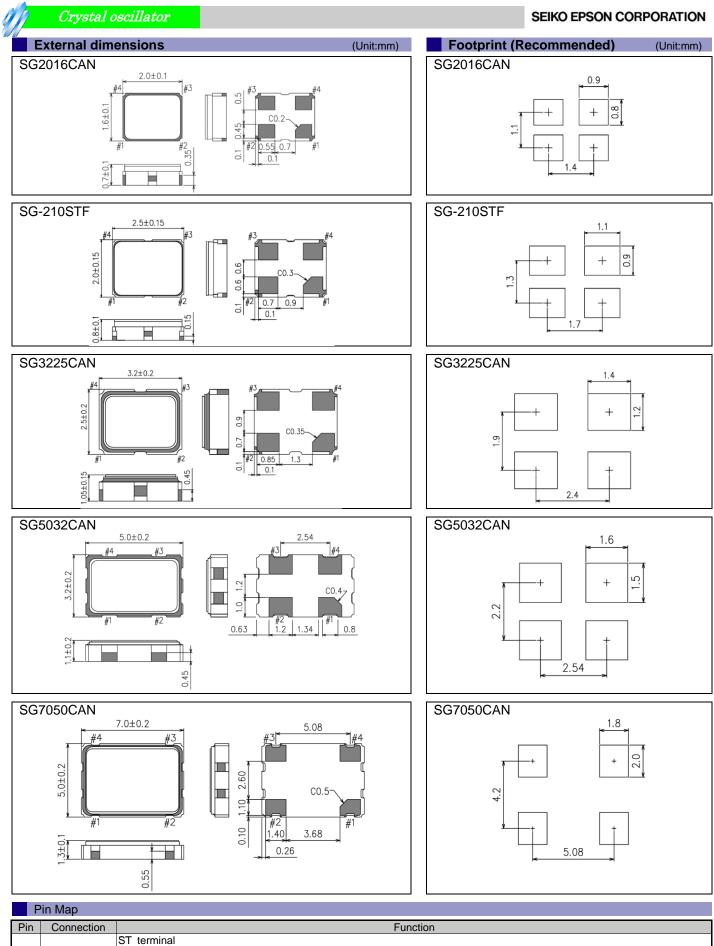
т

Product name	<u>SG-210 S T</u>	F 25.000000MHz Y
(Standard form)	0	4 5
①Model ②Funct	tion(S:Standb	y) ③Supply voltage
④Frequency ⑤F	Frequency tole	erance
③Supply voltage	*See Figure 1	⑤Frequency toler

Figure 1 : The upper limit of Operating temperature and the related conditions Please note that Supply voltage range (Vcc) depends on Output frequency (fo) and upper limit of

Operationg temperature (T_use Max.).

equency (5) Frequency tolerance						
Supply voltage *See Figure 1	⑤Frequency tolerance					
1.8 V to 3.3 V Typ.	S* ±25 × 10 ⁻⁶ / -20 °C to +70 °C					
	L ±50 × 10 ⁻⁶ / -40 °C to +85 °C					
	Y ±50 × 10 ⁻⁶ / -40 °C to +105 °C					
* Please refer to Product number list on Full Data Sheet for available frequencies						



		ST terr	ninal				
1	1 ST		ST function	Oscillator circuit	Output		
1 '			HIGH or "open"	Oscillation	Specified frequency: Enable		
			LOW	Oscillation stop	High impedance: Disable		
2	GND	Ground					
3	OUT	Clock output					
4	V _{cc}	Power s	supply				

Notes: To maintain stable operation, provide a 0.01uF to 0.1uF by-pass capacitor at a location as near as possible to the power source terminal of the crystal product (between Vcc - GND).

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